

BIODATA

- 1. NAME** : Prof. Ravikumar. K. I
- 2. DATE OF BIRTH** : 28-11-1988
- 3. ADDRESS,**
OFFICE/COLLEGE : Jain Institute of Technology
#323, Near Veereshwara Punyashrama,
Davangere-577005
- RESIDENCE** : #1970/ 42, 2nd cross, 2nd main,
S. S Layout B Block,
Davangere
- 4. CONTACT NO** : 9916247923
- 5. EMAIL ID** : ravi.iliger@yahoo.com
- 6. DESIGNATION** : Assistant Professor
- 7. DEPARTMENT** : Electronics and Communication Engineering
- 8. DATE OF JOINING** : 22-July-2013
- 9. EXPERIENCE (YEARS)**
- TEACHING** : 5 years
- RESEARCH** : 1 year

10. QUALIFICATIONS:

	University	Specialization	Year of passing	Class
B. E	V. T. U	Instrumentation Technology	2010	First class
M. Tech	V. T. U	VLSI Design and Embedded Systems	2012	First class with Distinction

11. EXPERIENCE:**(reverse chronological order)**

Positions	Institution/Industry	Period	Nature of Job	No of Years
Assistant Professor	Jain Institute of Technology	From 22 July 2013 to Till Date	Teaching	5 Years

12. FACULTY DEVELOPMENT PROGRAMMES/TRAINING/WORKSHOP/SUMMER/WINTER SCHOOLS ATTENDED (reverse chronological order)

SI No	Name of training/workshop	Year/Duration	Organizers
1	2- Day Workshop on ARM cortex M3 Microcontroller	2018	ISTE, BIET, AES Bangalore
2	3 – Day Faculty Development Program on Advances in VLSI Design	2017	Bangalore Institute of Technology, Bangalore
3	1 - Day workshop on Plagiarism Issues	2016	GM Institute of Technology, Davangere
4	3 - Day Learning and Development Intervention on Feel Teacher	2016	CLHRD, Mangaluru
5	1 - Week Faculty Development Programme on Advances in Power Electronics & Drives	2015	University B. D. T College of Engineering, Davangere
6	2 –Day Faculty Development Programme on Industrial Digital Design with Verilog HDL	2014	Alva’s Institute of Engineering and Technology, Mangalore

7	5 - Day National workshop on Nano-materials and Microsystems	2014	Nitte Meenakshi Institute of Technology, Bangalore.
8	3 - Day workshop on Cadence Software Training	2011	Cadence Design System (I) Pvt. Ltd., Bangalore
9	1 - Day workshop on Industrial Automation	2007	Industry Institute Partnership Cell of BIET & Prolific Systems Technologies Mumbai

13. FACULTY DEVELOPMENT PROGRAMMES/TRAINING/WORKSHOPS/SUMMER/WINTER SCHOOLS ORGANISED (reverse chronological order)

SI No.	Title/topic	Year & Duration	Funded by	Organizers
1	3 – Day workshop on Arduino & Wireless Embedded System	2016		Jain Institute of Technology, Davangere
2	1 - Day Faculty Development Programme on Plagiarism Issues and Essential Tools for Research Write-ups	2015		Jain Institute of Technology, Davangere

14. CONFERENCE /SEMINARS/PROFESSIONAL SOCIETY ACTIVITIES/EVENTS ATTENDED (reverse chronological order)

SI No.	Title/topic	Year & Duration	Funded by	Organizers
1	Design of CMOS VLSI using Novel Low Power State Retention Technique	2016		National conference on Recent Trends in Communication and Computing (NCRTCC-16) organized by Jain Institute of Technology, Davangere

2	Design and Verification using Low Leakage 8T SRAM: 16bit Low Power system	2015		National Conference on Advanced Communication, VLSI Design & Signal Processing (NCCVS-15), organized by K. S. School of Engineering & Management, Bangalore.
3	Design of MRAM using Spintronics	2011		National Conference on Evolutionary Trends in Information Technology (CETIT), PP-107 , conducted by Visvesvaraya Technological University, Belgaum

**15. CONFERENCES/SEMINARS/PROFESSIONAL SOCIETY ACTIVITIES/EVENTS ORGANIZED
(reverse chronological order)**

SI No.	Title/topic	Year & Duration	Funded by	Organizers
1	National conference on Recent Trends in Communication and Computing (NCRTCC-16)	2016		Jain Institute of Technology, Davangere

16. PAPERS PUBLISHED:

SI No	Type of publication	No. of publications
1	International Journal	4
2	National Conferences	3

**17. DETAILS OF PAPER PUBLISHED (along with the DOI and publication/citation details)
(reverse chronological order)**

SI No	Title of paper/Report	Author (s)	Name & volume of Journal & Year
1	Traffic Stack-Up control using Renesas Microcontroller	Ravikumar K I	International Journal of Advanced Research in Electronics and Communication Engineering (IJARECE)

2	Novel Low Power Logic Gates using Sleepy Techniques	Ravikumar K I	International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering (IJAREEIE) January 2015
3	Low Power SRAM Memory System using Low Leak Asymmetric SRAM Cell	Ravikumar K I	International Journal of Recent Trends in Engineering and Technology (IJRTET) ACEEE July 2013
4	Design and Verification of Low Power 64bit SRAM System using 8T SRAM: Back-End Approach	Ravikumar K I	International Journal of Engineering and Innovative Technology (IJEIT) ISSN: 2277-3754, June 2012

18. AWARD / RECOGNITION

Secured Distinction with V.T.U Third Rank in M. Tech

Best Paper at National conference on Recent Trends in Communication and Computing 2016

19. NUMBER OF PROJECTS GUIDED

UG: 8 Projects

20. SUBJECTS TAUGHT

1. Analog Electronic Circuits,
2. Fundamentals of CMOS VLSI,
3. Control Systems
4. Microelectronics Circuits
5. Digital Switching Systems

21. BRIEF ON ACTIVITIES FOR INSTITUTIONAL DEVELOPMENT

Internal Assessment Coordinator and Examination Management System Coordinator in ECE Department and have successfully planned, organized and coordinated various events and functions of the college